Please amend the claims as follows (this listing of claims replaces all prior versions):

1. (Canceled)

2. (Currently Amended) A display as claimed in claim [[1]] 10, wherein the means for converting comprises, within each pixel, at least first and second display elements having different areas.

3. (Original) A display as claimed in claim 2, wherein the first and second display elements have areas in the ratio 1:2.

4. (Currently Amended) A display as claimed in claim [[1]] 10, wherein the means for converting comprises, within each pixel, charge redistribution circuit elements.

5. (Original) A display as claimed in claim 4, wherein the charge redistribution elements comprise two display elements, an input switch between the input to the pixel and a first display element and a charge redistribution switch between the first and second display elements.

6. (Canceled)

7. (Canceled)

8. (Canceled)

9. (Canceled)

10. (Currently Amended) ~~A display as claimed in claim 9,~~ An active matrix display, comprising:

an array of pixels provided over a common substrate, each pixel comprising a display element and a switching device;

a column driver for providing signals to the pixels for driving the display elements, the column driver comprising digital to analogue converter circuitry providing at most 2^p display element drive levels from a p bit digital data signal, wherein p is a positive integer; and

a converter for deriving from a 6 bit drive signal a signal for selecting which one or ones of the display element drive levels to apply to each display element;

wherein each pixel comprises means for receiving the display element drive levels and converting the display element drive levels into a number of pixel grey levels greater than 2^p, and the converter comprises a divider for dividing the 6 bit drive signal by 3 and providing a divisor and a remainder.

11. (Previously Presented) A display as claimed in claim 10, wherein the divisor determines which of the first number of levels is applied to one or both of the display elements, and the remainder determines which one or ones of the display elements this determined level is applied to.

12. (Original) A display as claimed in claim 11, wherein an adjacent level is applied to the display elements (if any) to which the determined level is not applied.

13. (Currently Amended)A display as claimed in claim [[1]] 10, comprising a plurality of row conductors, a number of row conductors being associated with each row of pixels corresponding to the number of display elements within each pixel.

14. (Currently Amended)A display as claimed in claim [[1]] 10, wherein each pixel comprises a memory element for storing digital drive values for the display elements of each pixel.

15. (Currently Amended)A display as claimed in claim [[1]] 10, wherein the digital to analogue circuitry is provided on the common substrate.

16. (Original) A display as claimed in claim 15, wherein the pixel array and the digital to analogue circuitry are formed using low temperature polysilicon processing.

17. (Canceled)

18. (Canceled)

19. (Currently Amended) A method ~~as claimed in claim 18,~~ of driving an active matrix display, comprising:

providing first and second drive voltages to a display pixel having first and second display elements, the first and second drive voltages being selected from two adjacent drive voltage levels of a digital to analogue converter that provides at most 2^p display element drive levels from a p bit digital data signal, wherein p is a positive integer; and

within the pixel, generating an intermediate grey level corresponding to a drive voltage between the first and second levels,

wherein each of the first and second display elements is configured to receive one of the first and second drive voltages, and the first and second drive voltages are provided by a digital to analogue converter which receives a 5 bit input derived from a 6 bit data signal by dividing the 6 bit data signal by 3 and providing a divisor and a remainder.

20. (Original) A method as claimed in claim 19, wherein the divisor determines the first drive voltage, and the remainder determines whether the first and second drive voltages are the same or are different.

21. (Canceled)

22. (Canceled)

23. (Currently Amended) A method as claimed in claim [[17]] 19, wherein charge sharing between the display elements is used to generate the intermediate grey level.

24. (Original) A method as claimed in claim 23, wherein the first and second drive voltages are provided by a digital to analogue converter which receives a 5 bit input.

25. (Currently Amended) A method as claimed in claim [[17]] 19, wherein the drive voltages are provided from a column driver circuit integrated onto the active plate of the active matrix display.

26. (Currently Amended) A method as claimed in claim [[17]] 19, wherein each of the first display element and the second display element selectively receives the first drive voltage or the second drive voltage to enable the display pixel to generate four pixel grey levels.

27. (Canceled)

28. (New) A display comprising:

a plurality of pixels, each pixel comprising a display element;

a digital to analog converter to provide a first number of display element drive levels, the first number being greater than 2; and

a converter to derive from a q1 bit drive signal a signal to select which one of the first number of levels to apply to each display element, the converter comprising a divider to divide by q2 and provide a divisor and a remainder, q1 and q2 being integers greater than 1;

wherein each pixel converts the first number of display element drive levels into a second, greater number, of pixel grey levels.

29. (New) The display of claim 28 in which q1 is equal to 6.

30. (New) The display of claim 28 in which q2 is equal to 3.

31. (New) The display of claim 28, comprising a column driver for providing signals to the pixels for driving the display elements, the column driver comprising the digital to analog converter.

32. (New) The display of claim 28 in which each pixel comprises at least first and second display elements having different areas.

33. (New) The display of claim 32 in which the divisor determines which of the first number of levels is applied to one or ones of the at least first and second display elements, and the remainder determines which one or ones of the at least first and second display elements this determined level is applied to.

34. (New) The display of claim 28 in which the first and second display elements have areas in the ratio 1:2.

35. (New) A method comprising:

receiving an r1 bit input derived from an r2 bit data signal by dividing the r2 bit data signal by r3 and providing a divisor and a remainder, r1, r2, and r3 being integers greater than 1;

determining first and second drive voltages based on the divisor and the remainder; and

selectively providing the first and second drive voltages to a display pixel having first and second display elements.

36. (New) The method of claim 35, comprising, within the display pixel, generating an intermediate grey level corresponding to a third drive voltage between the first and second drive voltages.

37. (New) The method of claim 36 in which charge sharing between the display elements is used to generate the intermediate grey level.

38. (New) The method of claim 35 in which the first and second drive voltages are selected from two adjacent drive voltage levels of a digital to analog converter that has more than 2 output levels.

39. (New) The method of claim 35 in which r1 equals 5.

40. (New) The method of claim 35 in which r2 equals 6.

41. (New) The method of claim 35 in which r3 equals 3.

42. (New) The method of claim 35 in which the divisor determines the first drive voltage, and the remainder determines whether the first and second drive voltages are the same or are different.

43. (New) The method of claim 35 in which the first display element has a first area and the second display element has a second area that is different from the first display area.

44. (New) The method of claim 43, comprising within the display pixel, generating an intermediate grey level corresponding to a third drive voltage between the first and second drive voltages using area weighting.

45. (New) The method of claim 35 in which the first and second drive voltages are selected from two adjacent drive voltage levels of a digital to analog converter that has more than 2 output levels.

46. (New) The method of claim 35 in which a plurality of sub-rows of pixels are addressed in turn, each sub-row comprising respective display elements for each pixel.

47. (New) The method of claim 35 in which a plurality of rows of pixels are addressed in turn, each row being addressed once to address both display elements and a second time to readdress the second display element.

48. (New) The method of claim 35 in which each of the first display element and the second display element selectively receives the first drive voltage or the second drive voltage to enable the display pixel to generate four pixel grey levels.

49. (New) The method of claim 35 in which the first display element and the second display element both receive the first drive voltage or the second drive voltage.